

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Chung-Kuan CHENG  
Serial No.: 10/526,523  
Conf. No.: 5310  
Filed: March 1, 2005  
For: INTERCONNECTION ARCHITECTURE  
AND METHOD OF ASSESSING  
INTERCONNECTION ARCHITECTURE  
Art Unit: 2818  
Examiner: Nguyen, Thinh T.

INFORMATION DISCLOSURE STATEMENT

Mail Stop RCE  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

This IDS is submitted under 37 C.F.R. §1.97(b) within any of the following time periods, whichever occurs last:

- (a) within three months of either the filing date of the application or the date of entry into the national stage; or
- (b) before the mailing date of First Office Action on the merits (i.e., not including actions such as restriction requirements); or
- (c) before the mailing of a First Office Action after the filing of a Request for Continuing Examination.

Applicant(s) submit herewith Form PTO-1449 (Information Disclosure Citation) together with copies of foreign patents, publications or other information of which applicant(s) are aware, which applicant(s) believe may be material to the examination of this application and for which there may be a duty to disclose in accordance with 37 C.F.R. §1.56. Applicant(s) respectfully submit that the citation of any reference on Form PTO-1449 does not constitute an admission that the reference qualifies as prior art.

It is requested that the information disclosed on the enclosed Form PTO-1449 be made of record in this application.

The Commissioner is hereby authorized to charge any additional fees which may be required to this application under 37 C.F.R. §§1.16-1.17, or to credit any overpayment, to Deposit Account No. 07-2069.

Respectfully submitted,

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June 30, 2009  
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By: 

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